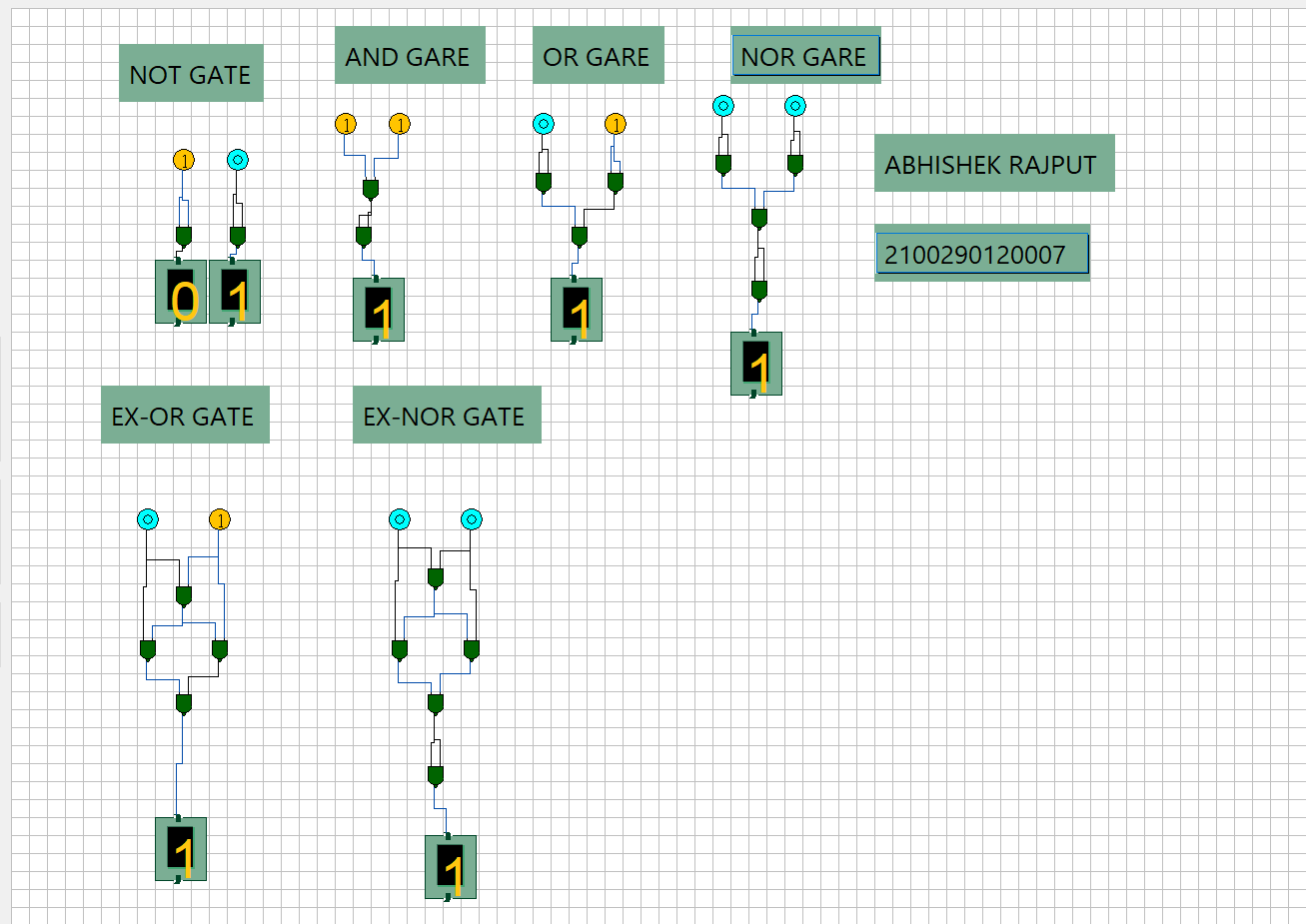
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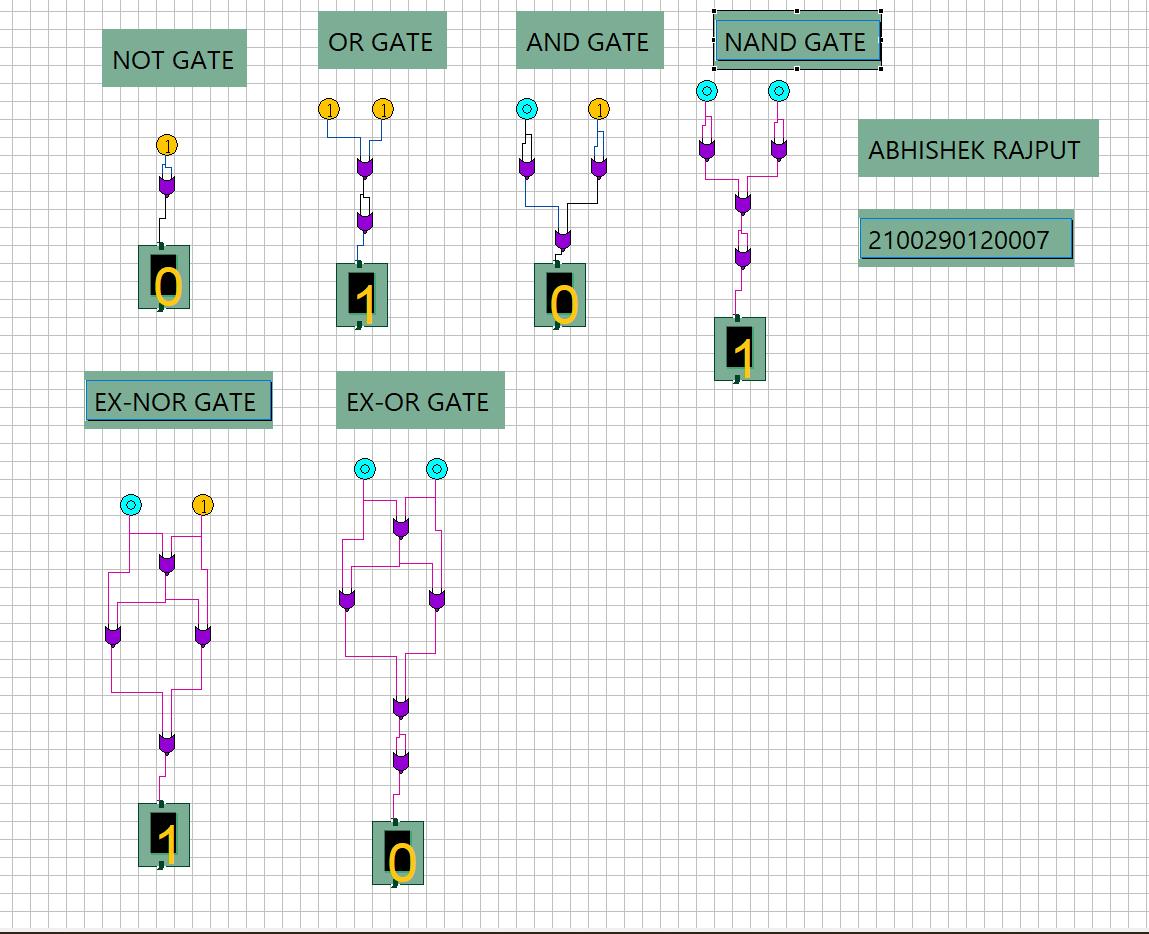
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**IMPLEMENTATION OF LOGIC GATES USING NAND GATE**



**IMPLEMENTATION OF LOGIC GATES USING NOR GATE**

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